

Perp. to Fin view

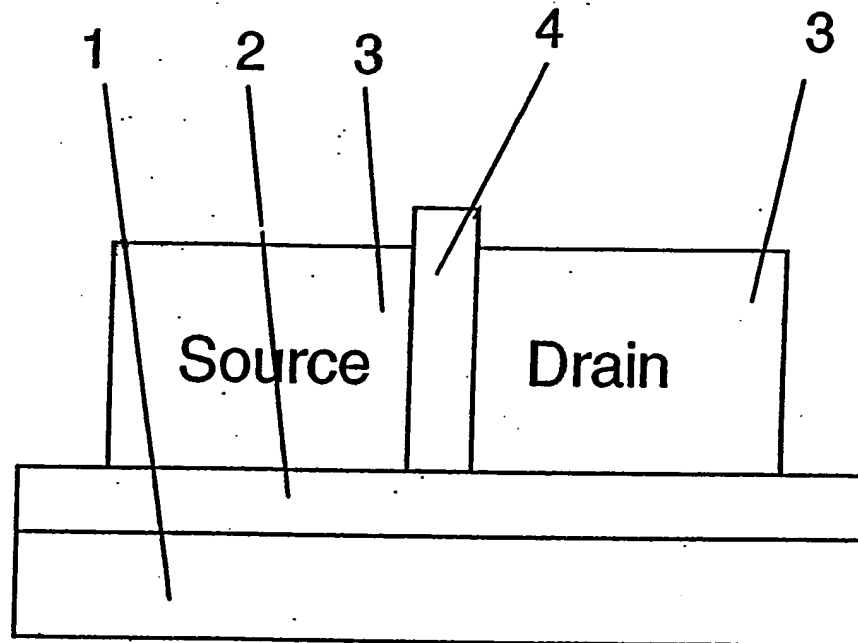


Fig 1  
Prior Art

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Parallel to Fin view

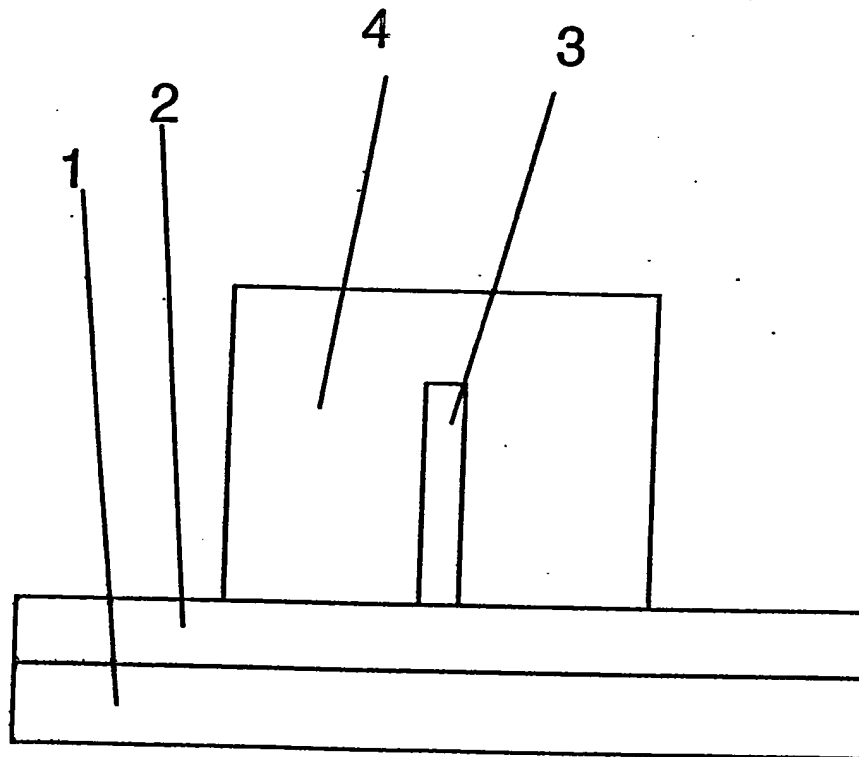
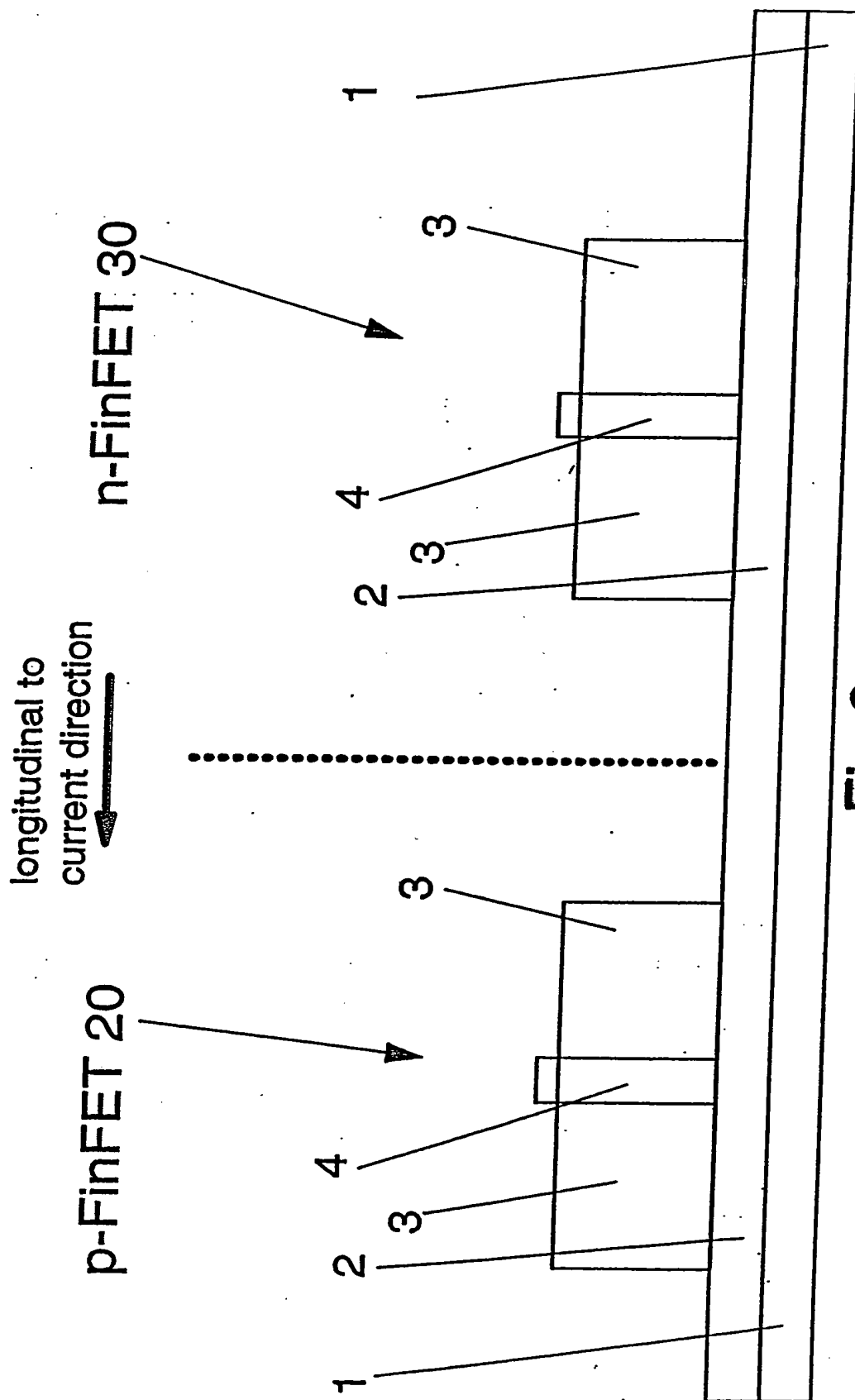


Fig 2  
Prior Art



**Fig 3**  
**Prior Art**

# Deposit Thin SiO<sub>2</sub> Liner

Perp. view

p-FinFET      n-FinFET

longitudinal to  
current direction

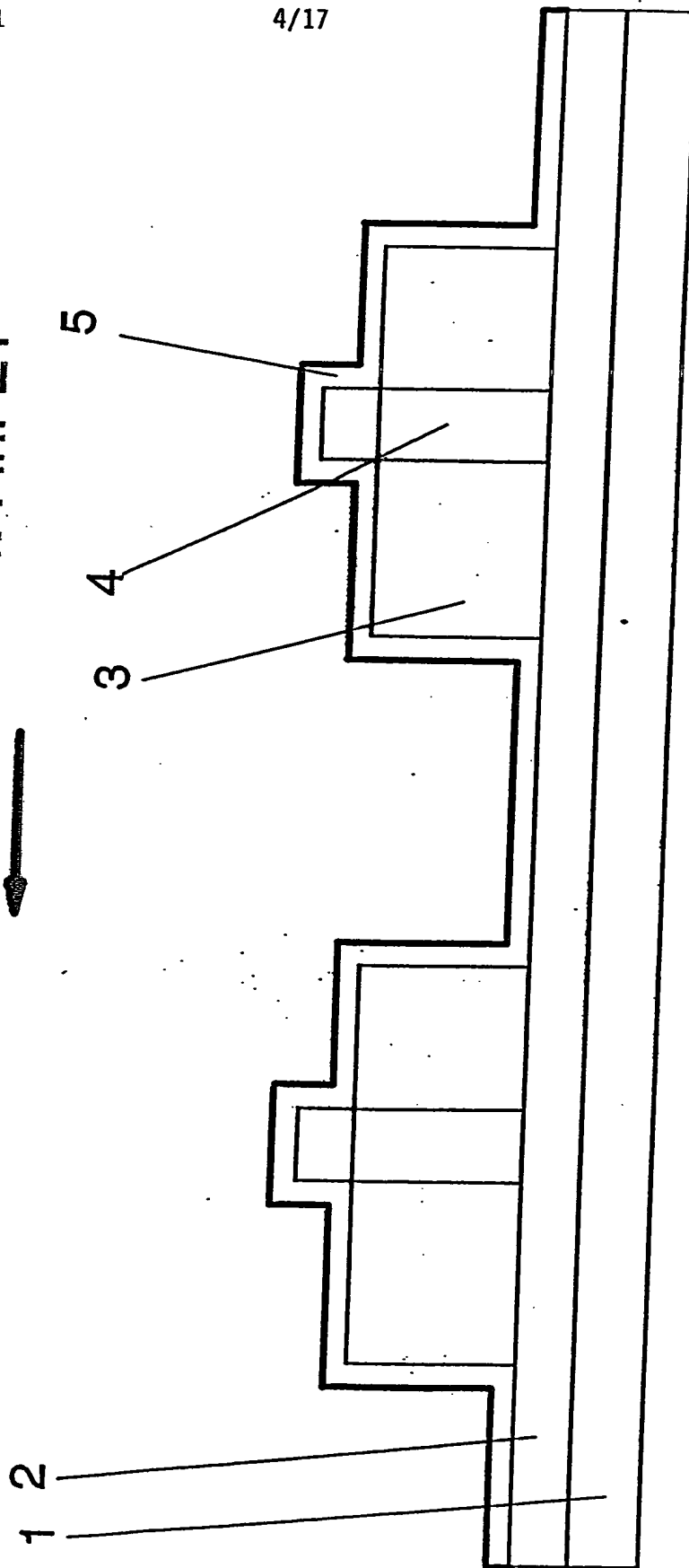


Fig 4

Parallel view

Deposit Thin SiO<sub>2</sub> Liner

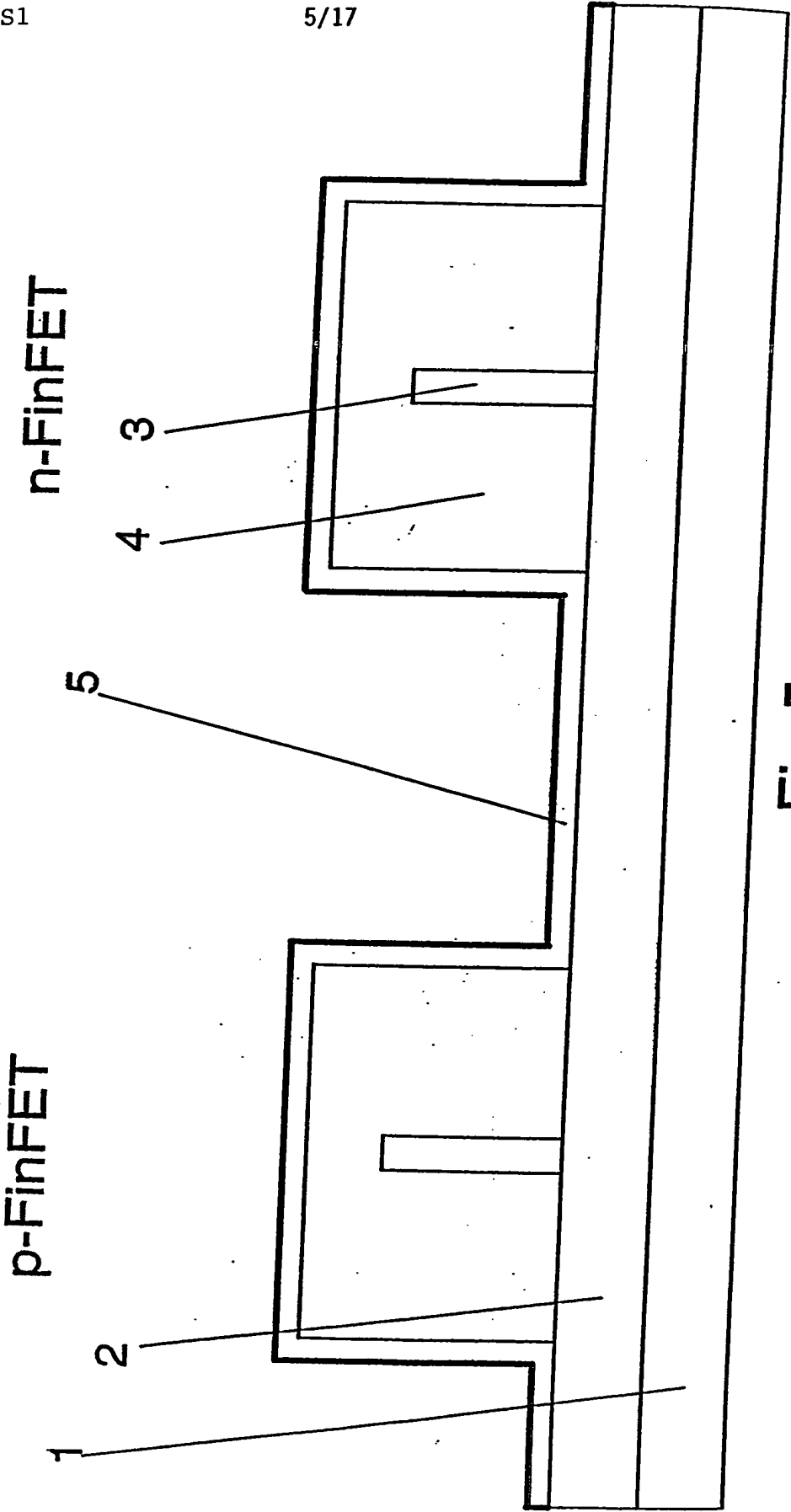


Fig 5

# Deposit Compressive Film

Perp. view p-FinFET

n-FinFET

longitudinal to  
current direction

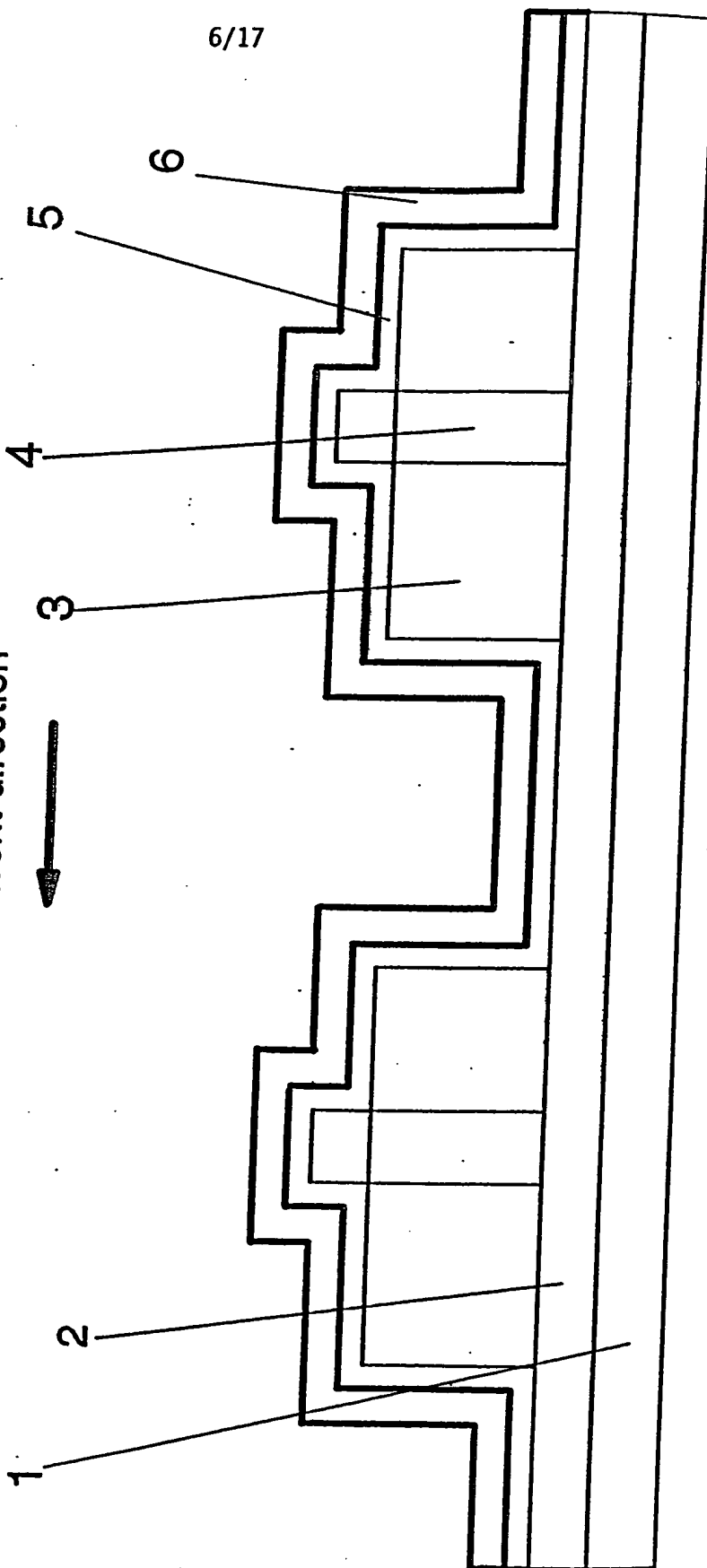


Fig 6

FIS920020105US1

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# Deposit Compressive Film

Parallel view

p-FinFET

n-FinFET

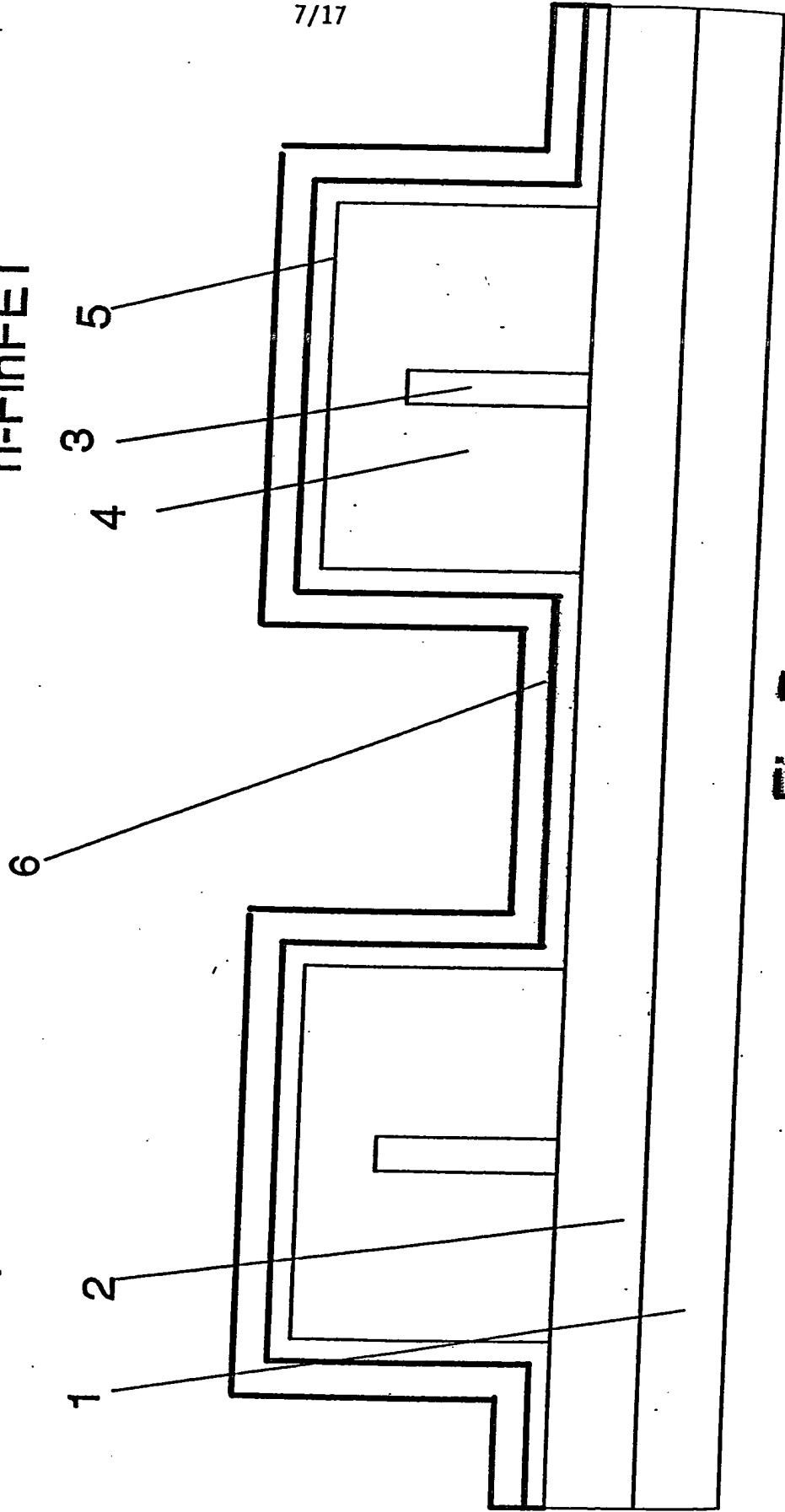
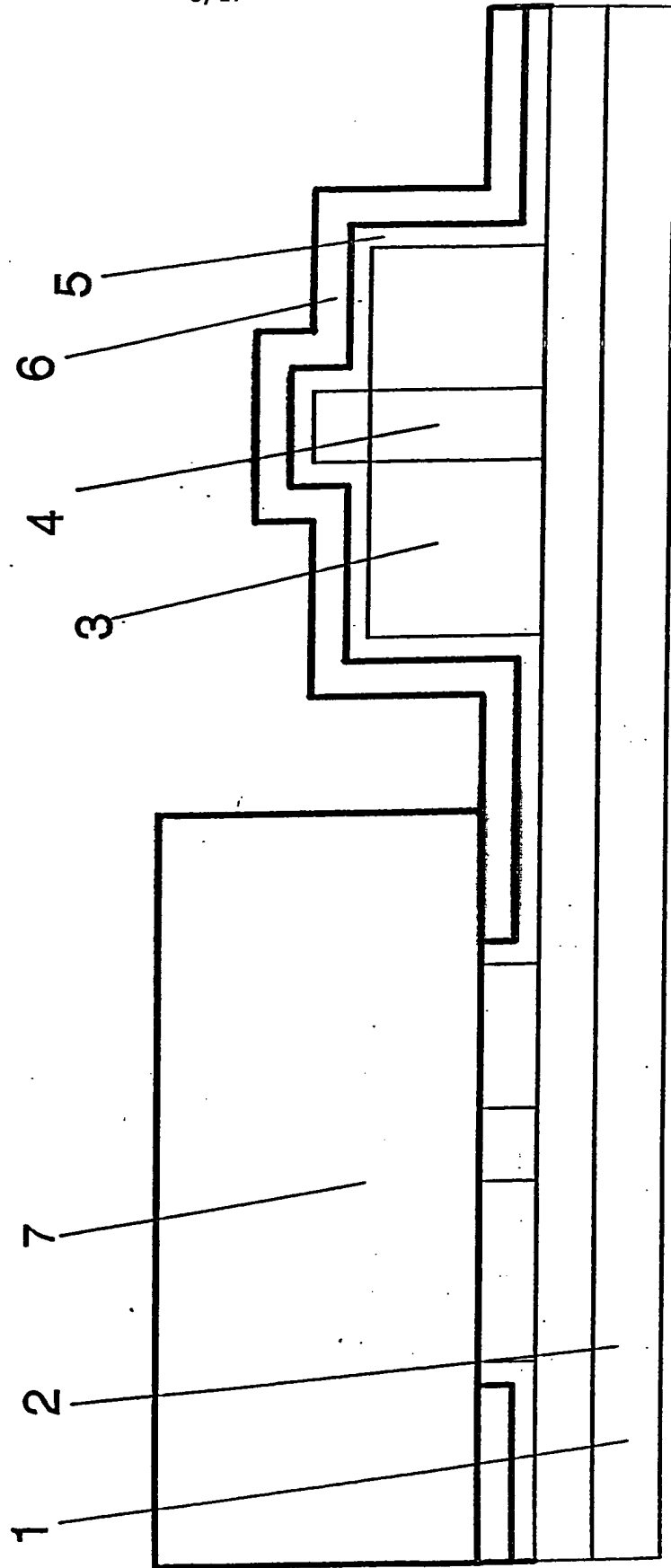


Fig 7

# Perp. view      Apply Block Mask

p-FinFET      n-FinFET

longitudinal to  
current direction  
↓



**Fig 8**



# Apply Block Mask

FIS920020105US1

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Parallel view

p-FinFET

n-FinFET

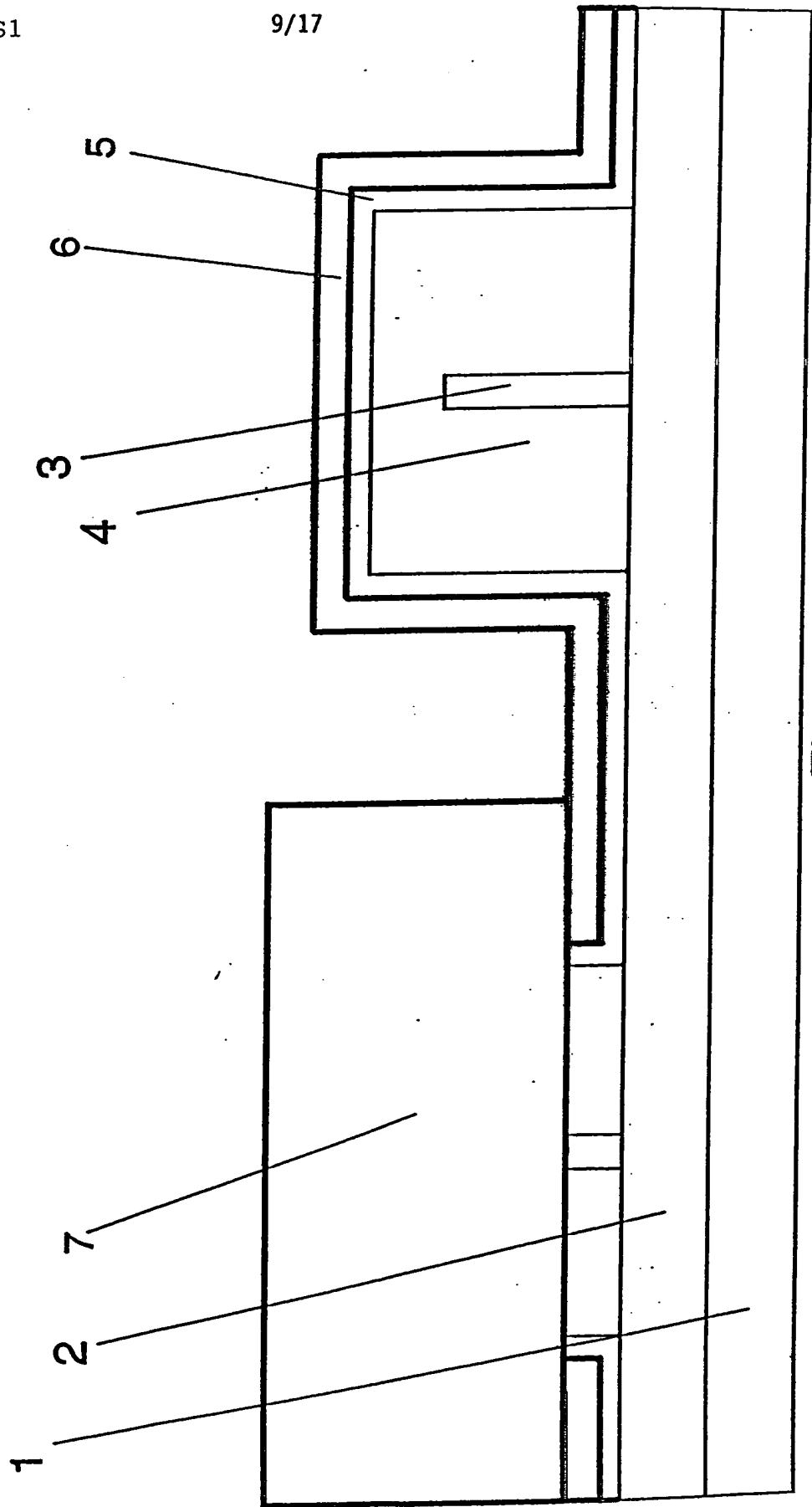


Fig 9

# Remove Compressive Film From n-FinFET Stop on Liner

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Perp. view

p-FinFET

n-FinFET

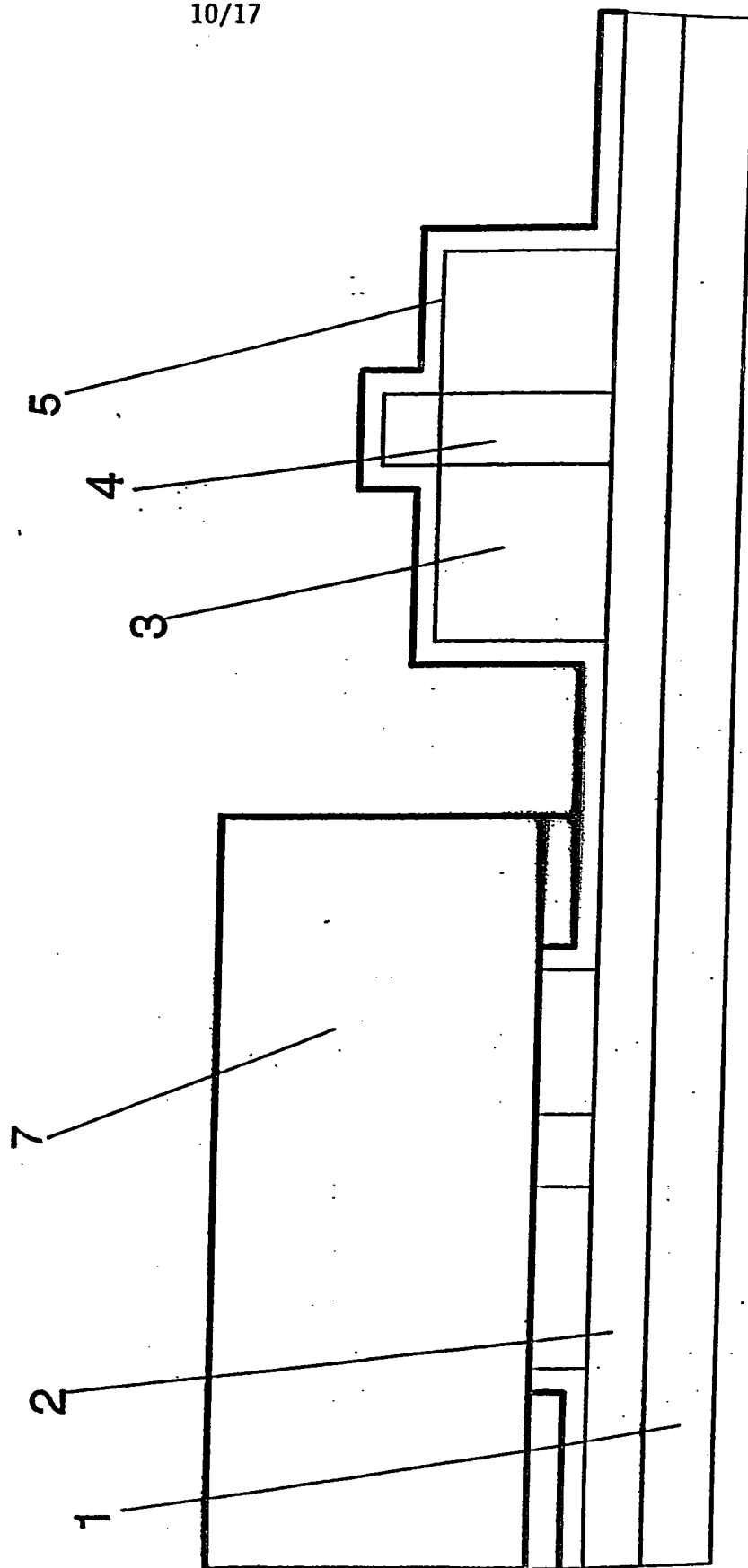


Fig 10

# Remove Compressive Film From n-FinFET Stop on Liner

Parallel view

longitudinal to  
current direction  
↓  
n-FinFET

p-FinFET

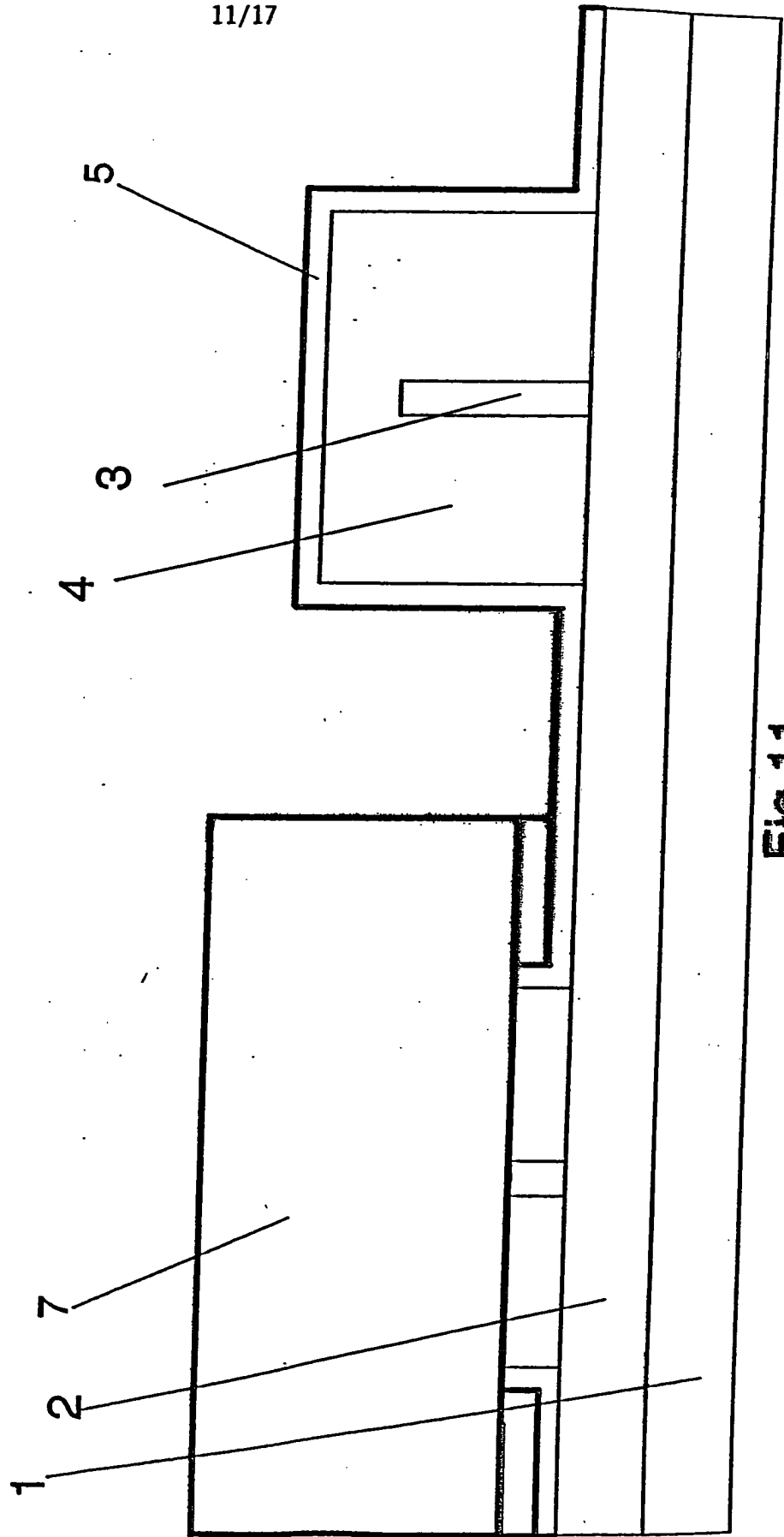


Fig 11

FIS920020105US1

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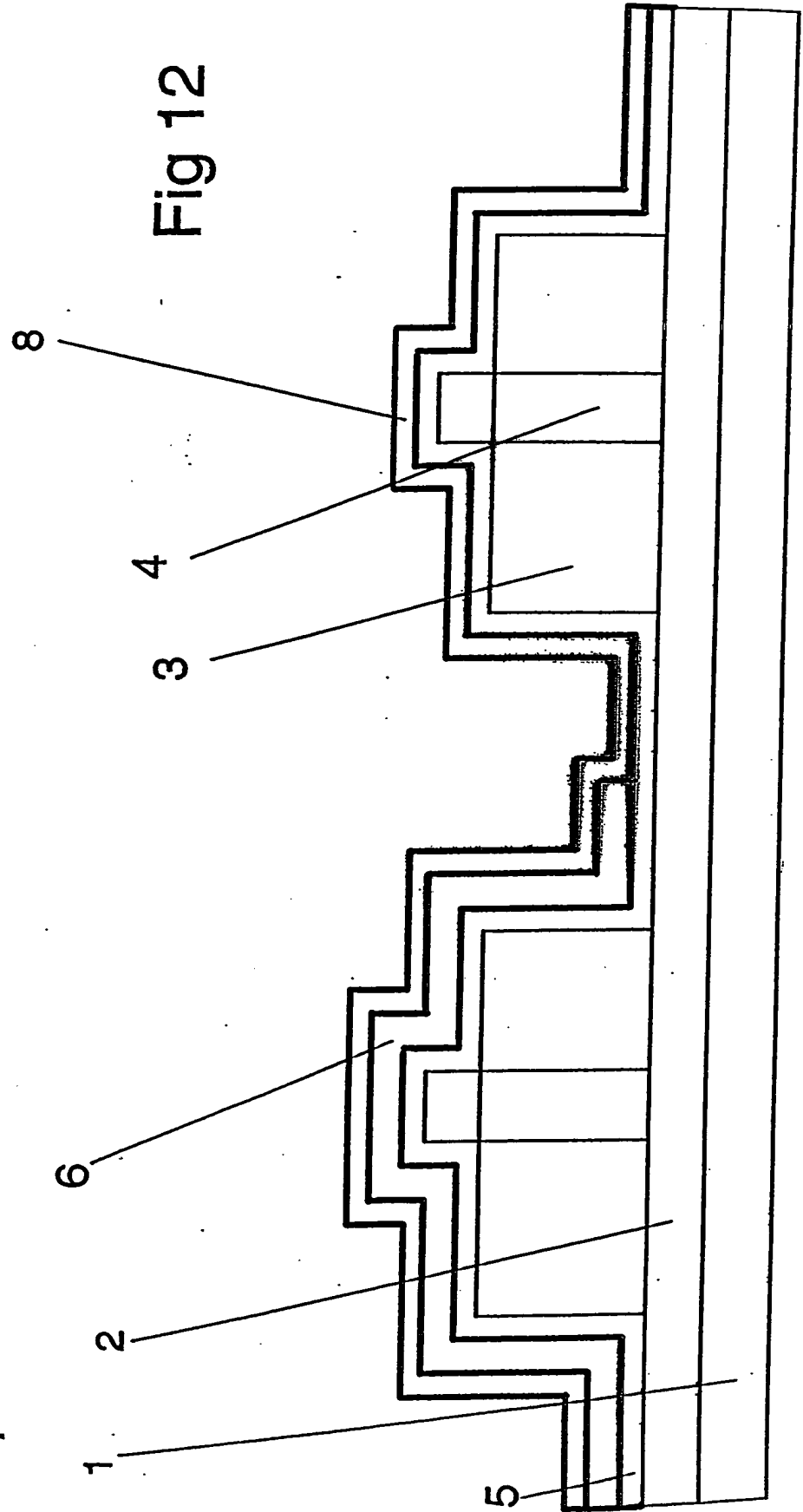
# Remove Block Mask, Optional Liner 1 Removal, Deposit 2nd thin liner

Perp. view

longitudinal to  
current direction  
→

n-FinFET

p-FinFET





# Deposit Tensile Film

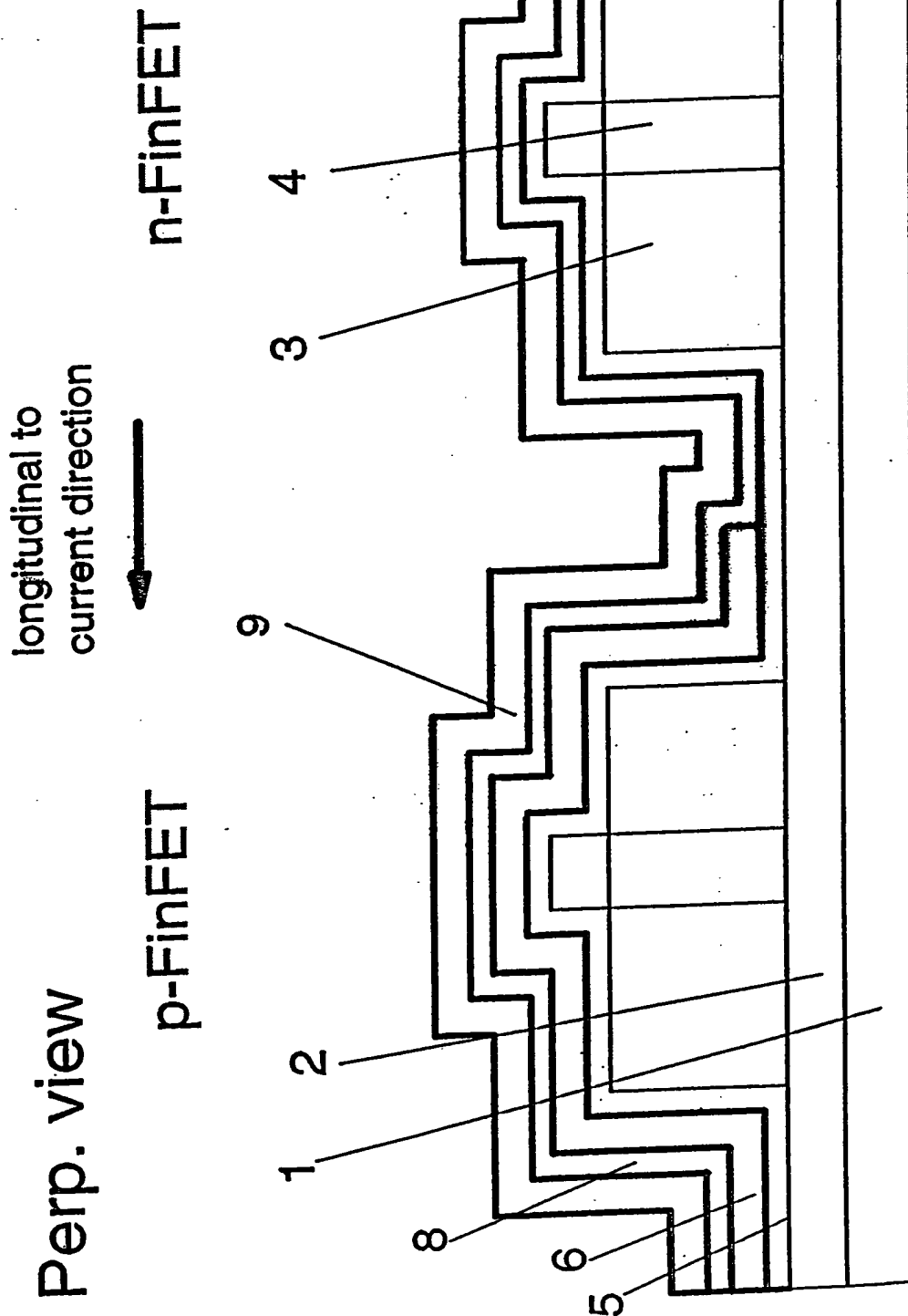


Fig 14

# Deposit Tensile Film

Parallel view

p-FinFET

n-FinFET

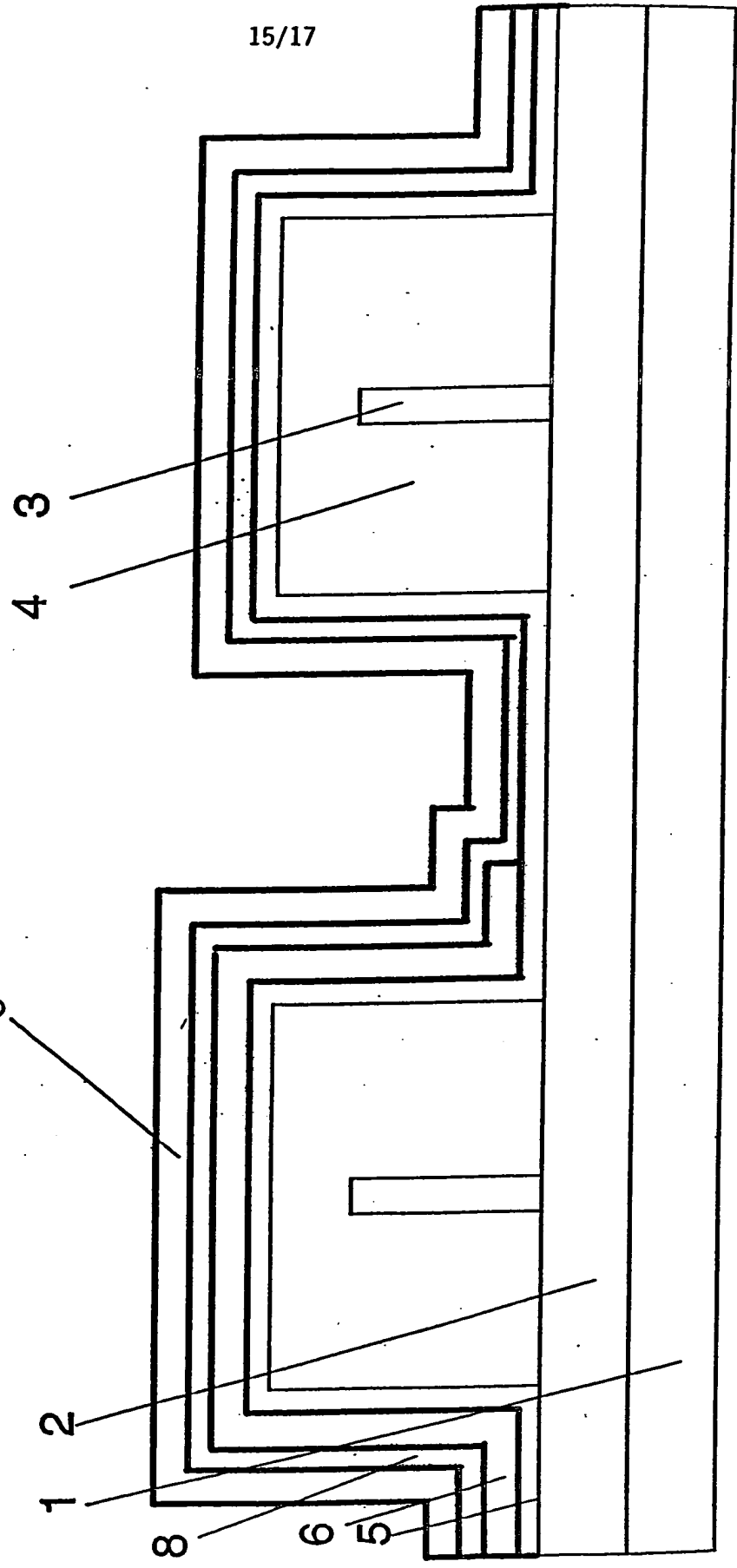


Fig 15

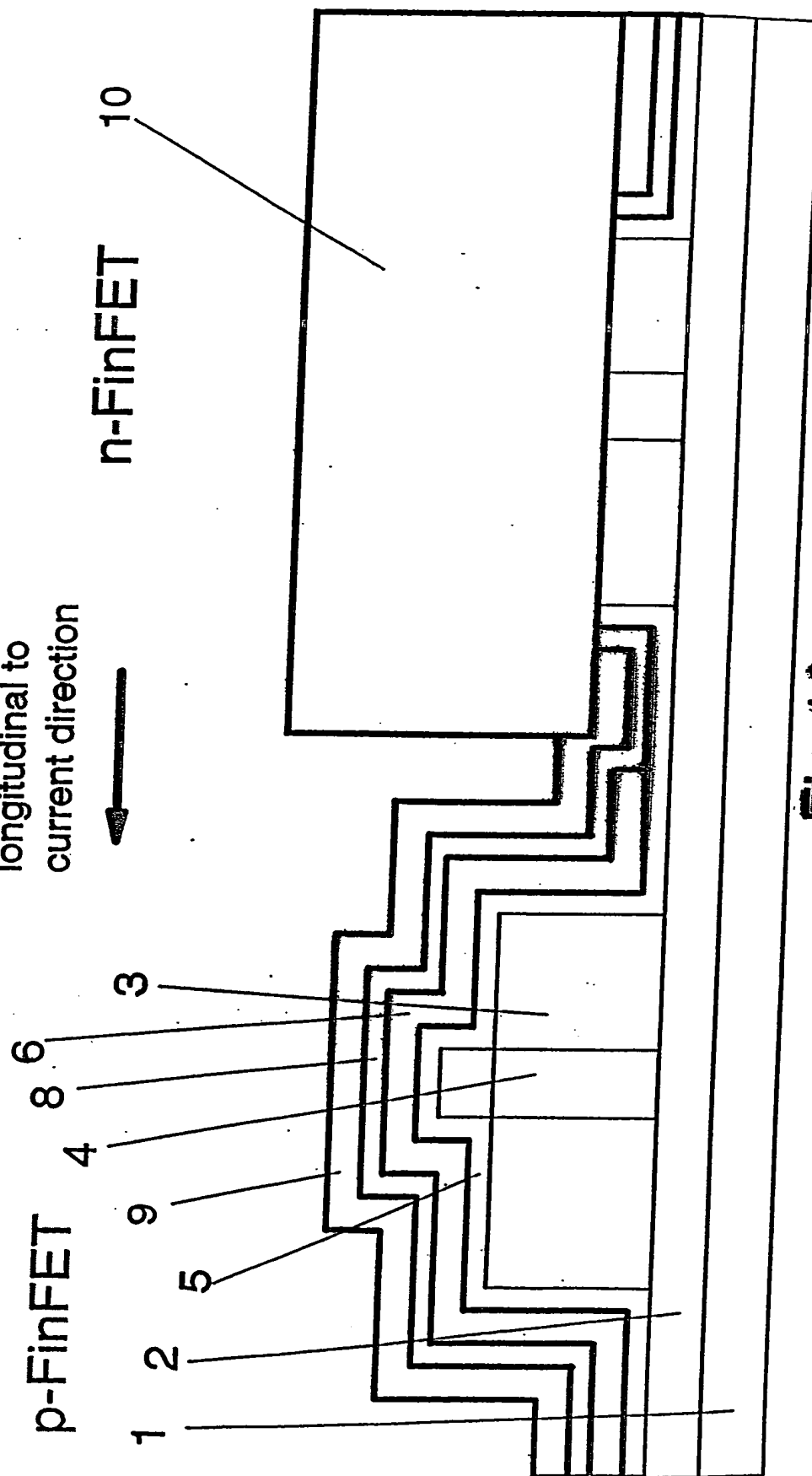
Perp. view

# p-FinFET

**longitudinal to  
current direction**

# n-FinET

10

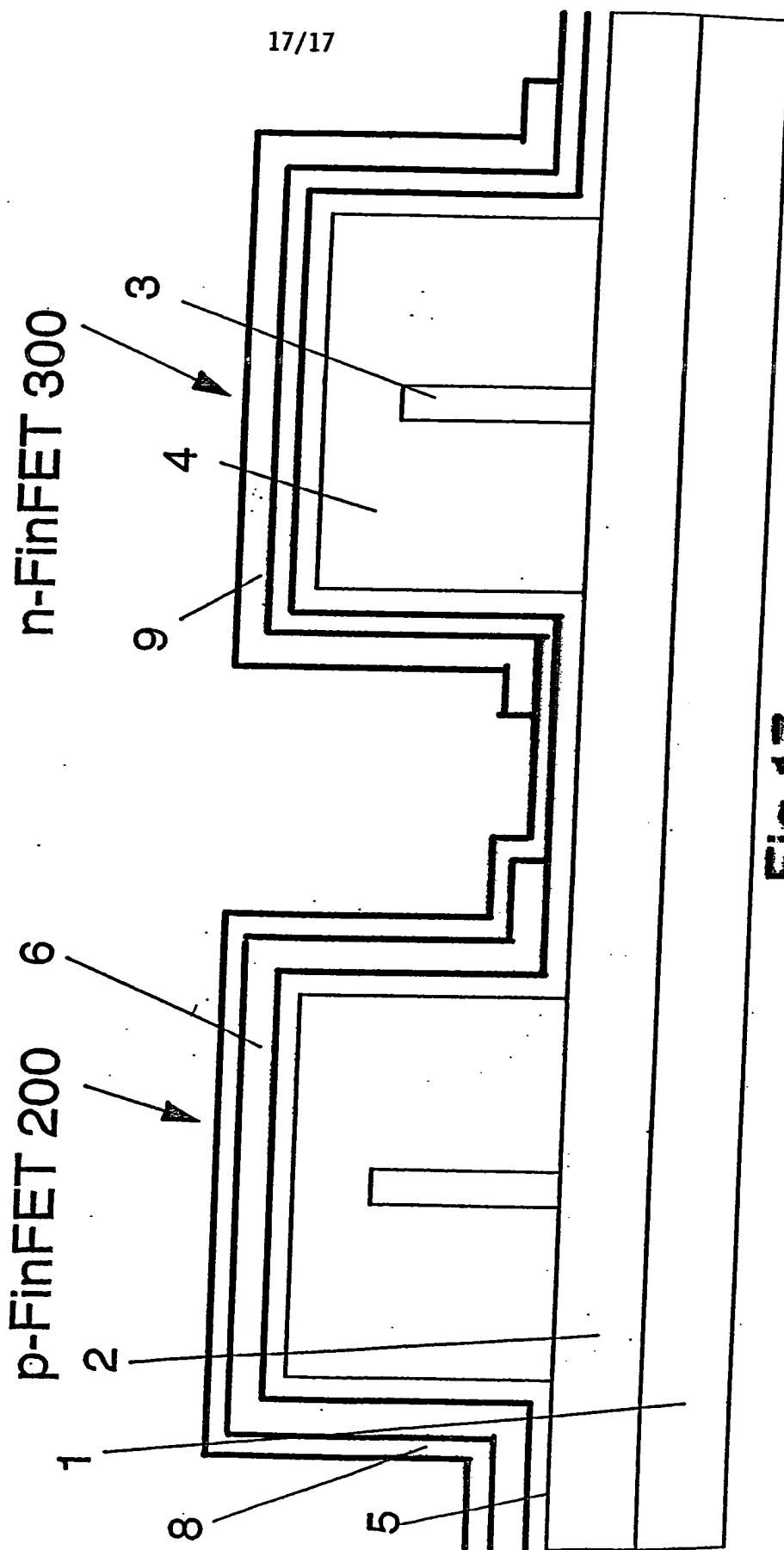


CO  
—  
OF



**Apply Block Mask and Remove Tensile Film From  
p-FinFET, Remove Block Mask  
Parallel view Final Device Structure**

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**Fig 17**

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